1 Classical Computers

- Q.1 Give a reversible circuit to add two single-bit numbers x and y, giving the output as a two-bit number.
- S.1 Note that there is never a unique circuit but in this case the obvious simple circuit is



In this case the order of the gates doesn't matter. The CCNOT get sets $a_1 = 1$ if x = y = 1 while the two CNOT gates set $a_0 = 1$ if precisely one of x = 1 or y = 1.

- Q.2 List all possible single-bit functions of a two-bit input x (so $f(x_1x_0)$ is 0 or 1 for each input). Give reversible circuit representations using the universal gate set {NOT, CNOT, CCNOT} for all such functions with f(00) = 0. State a simple modification of these circuits to produce circuits for all such functions with f(00) = 1. Given that {NOT, CNOT} is not a universal gate set, is it possible to construct all the functions without using CCNOT?
- S.2 There are four possible values for x, and f(x) has two possible values for each choice, so there are $2^4 = 16$ functions which we can label f_0, f_1, \ldots, f_{15} . For the circuits we can take 3 bits in total, the two input bits and another bit initialised to 0 which will give the output bit – it is not necessary to include any further (ancillary) bits. Taking $x = x_1x_0$, we can write CCNOT to mean a CCNOT gate with the output bit as the target and the two input bit as the controls, $CNOT_0$ ($CNOT_1$) to mean CNOT acting on the output bit controlled by x_0 (x_1), and NOT to mean a NOT acting on the output. You can then easily draw the circuits by placing these gates in the same order left to right. (Actually, if you use these gates only the order does not matter – in general the order is important!) These are not unique circuits so you may find different correct circuits. The following table summarises all the details. Note that the list of all outputs for f_N is just N written as a 4-digit binary in these conventions. You could define the functions in other ways such as by using combinations of logical operations, but this way it is manifest that we have included all possible functions

x	00	01	10	11	Representation	Logic output
f_0	0	0	0	0	Trivial	0
f_1	0	0	0	1	CCNOT	$x_0 AND x_1$
f_2	0	0	1	0	$CCNOT \ CNOT_1$	$(NOT x_0) AND x_1$
f_3	0	0	1	1	$CNOT_1$	x_1
f_4	0	1	0	0	$CCNOT \ CNOT_0$	$x_0 AND (NOT x_1)$
f_5	0	1	0	1	$CNOT_0$	x_0
f_6	0	1	1	0	$CNOT_0 \ CNOT_1$	$x_0 XOR x_1$
f_7	0	1	1	1	$CCNOT \ CNOT_0 \ CNOT_1$	$x_0 OR x_1$
f_8	1	0	0	0	$CCNOT \ CNOT_0 \ CNOT_1 \ NOT$	$x_0 NOR x_1$
f_9	1	0	0	1	$CNOT_0 \ CNOT_1 \ NOT$	$x_0 NXOR x_1$
f_{10}	1	0	1	0	$CNOT_0 NOT$	$NOT x_0$
f_{11}	1	0	1	1	$CCNOT \ CNOT_0 \ NOT$	$x_0 NAND (NOT x_1)$
f_{12}	1	1	0	0	$CNOT_1 NOT$	NOT x_1
f_{13}	1	1	0	1	$CCNOT \ CNOT_1 \ NOT$	$(NOT x_0) NAND x_1$
f_{14}	1	1	1	0	CCNOT NOT	$x_0 NAND x_1$
f_{15}	1	1	1	1	NOT	1

exactly once – the description in terms of logical operations is not unique.

Note that the second half (those with f(00) = 1) are the *NOT* of a function from the first half, specifically f_{15-N} is related to f_N in this way. So, if you have constructed circuits for the functions with f(00) = 0, you can simply include a NOT gate at the end of the output to produce the remaining circuits. If you have used the circuits described above, the NOT gate can be placed anywhere on the output line – but note this is not true in general.

These realisations are not unique, but we cannot avoid using CCNOT for all of them. In terms of the information given in the question, the simple argument is that f_1 implements CCNOT. If we could construct it from just $\{NOT, CNOT\}$ then we would be able to use that circuit anywhere we wanted a CCNOT gate. Hence, we would have shown that $\{NOT, CNOT\}$ is a universal gate set, since we are told $\{NOT, CNOT, CCNOT\}$ is. Clearly this contradicts the statement in the question so it must not be possible to construct a circuit for f_1 without using any CCNOT gates.

Actually, this argument is not quite correct since we only require the circuit to behave as CCNOT when the target is initialised to 0. This leaves the possibility that we could construct such a circuit without a CCNOT gate and it would behave as a CCNOT gate if the target was initially 0, but differently if the target was initially 1. However, it is easy to see that if we could construct any such a circuit, we could construct a CCNOT gate. To do this, take the circuit with the output bit initialised to 0. Then the output will be 0 unless both inputs were 1. This means that the output indicates whether or not the CCNOT gate (if output is 1) on the target of the CCNOT gate. So we can now take this output and use it as the control bit for a CNOT gate acting on another bit which is the target bit of the CCNOT gate which we have then constructed.

- Q.3 Give definitions of the complexity classes P, NP, PSPACE and EXP, and prove the inclusions $P \subseteq NP \subseteq PSPACE \subseteq EXP$.
- S.3 The definitions are bookwork. We interpret the inclusions in terms of problems. Any problem in P is clearly in NP; we can check that a solution is correct in polynomial time simply by solving the problem in polynomial time to see if the actual solution matches the

proposed solution. Any problem in NP is in PSPACE as we can simply check all the possible solutions one after the other until one works. This may take a very long time, but it will only require polynomial space since any algorithm in NP requires only polynomial resource. And everything is in EXP.

2 Quantum Computers

Q.4 Show that

$$R_{\hat{n}}(\theta) = \cos(\theta/2)I - i\sin(\theta/2)(n_xX + n_yY + n_zZ),$$

where $\hat{n} = (n_x, n_y, n_z)$ is a unit vector in \mathbb{R}^3 , is a unitary operator. Show that if a single qubit has the state

$$\hat{\rho} = \frac{1}{2}(I + \mathbf{r} \cdot \boldsymbol{\sigma}) = \frac{1}{2}(I + xX + yY + zZ),$$

where $\mathbf{r} = (x, y, z)$ is a unit vector (that is, this is a pure state), then the effect of the unitary operator $R_{\hat{n}}(\theta)$ is to rotate \mathbf{r} about the axis \hat{n} in the Bloch sphere by an angle θ .

S.4 To show it is unitary is just a calculation, but to show that we have a rotation can be done in different ways. Note first that conceptually we know the result must be a rotation since this is a unitary transformation of a single-qubit pure state – hence it must map the Bloch sphere to itself and preserve inner products (which are determined by relative positions on the Bloch sphere). The question is then, precisely what rotation is taking place.

There are several ways to approach this problem. A nice, but slightly abstract approach is to construct an argument by showing that $R_{(0,0,1)}(\theta)$ rotates by angle θ around the z-axis (which is a straightforward calculation), and then use symmetry to argue for the result. More precisely, we use the fact that we can always choose our coordinates or our basis vectors in \mathbb{R}^3 so that any given vector, taking $\hat{\mathbf{n}}$ in this case, is pointing along the new z-axis which we could label the z'-axis. Then, since the statement is not dependent on any specific choice of coordinates or basis, we have the result provided the operators $R_{\hat{n}}$ and $\hat{\rho}$ take the same form in any orthonormal basis. This is almost true. Under a change of basis we have $n_i \to n'_i = M_{ij}n_j$ and $r_i \to r'_i = M_{ij}\sigma_j$ then $\mathbf{r} \cdot \boldsymbol{\sigma} = r_i\sigma_i = r'_i\sigma'_i$ so the operators take the same form in any orthonormal basis provided we can interpret the σ'_i as Pauli σ -matrices. It is straightforward to check that indeed we have $\sigma'_i\sigma'_j + \sigma'_j\sigma'_i = 2\delta_{ij}I$ etc.

Below we outline a direct calculation.

We know X, Y, and Z are unitary, so

$$R_{\hat{n}}^{\dagger}(\theta) = \cos(\theta/2)I + i\sin(\theta/2)(n_xX + n_yY + n_zZ) = R_{\hat{n}}(-\theta)$$

Multiplying,

$$R_{\hat{n}}(\theta)R_{\hat{n}}(-\theta) = \cos^{2}(\theta/2)I + \sin^{2}(\theta/2)(n_{x}^{2}X^{2} + n_{x}n_{y}(XY + YX) + n_{x}n_{z}(XZ + ZX) + n_{y}^{2}Y^{2} + n_{y}n_{z}(YZ + ZY) + n_{z}^{2}Z^{2}.$$

Now the Pauli matrices satisfy XY + YX = XZ + ZX = YZ + ZY = 0, and $X^2 = Y^2 = Z^2 = I$, so

$$R_{\hat{n}}(\theta)R_{\hat{n}}(-\theta) = [\cos^2(\theta/2) + \sin^2(\theta/2)(n_x^2 + n_y^2 + n_z^2)]I = I$$

as $\hat{\mathbf{n}}$ is a unit vector. Thus, $R^{\dagger} = R^{-1}$, and this is a unitary operator.

Applying this transformation to $\hat{\rho}$,

$$\begin{split} \hat{\rho}' &= R^{\dagger} \hat{\rho} R = \frac{1}{2} [\cos(\theta/2)I + i\sin(\theta/2)(n_x X + n_y Y + n_z Z)](I + xX + yY + zZ) \\ &\times [\cos(\theta/2)I - i\sin(\theta/2)(n_x X + n_y Y + n_z Z)] \\ &= \frac{1}{2} \left\{ \cos^2(\theta/2)(I + xX + yY + zZ) + i\cos(\theta/2)\sin(\theta/2)[n_x X + n_y Y + n_z Z, xX + yY + zZ] \\ &+ \sin^2(\theta/2)[I + (n_x X + n_y Y + n_z Z)(xX + yY + zZ)(n_x X + n_y Y + n_z Z)] \right\} \\ &= \frac{1}{2} \left\{ I + \cos^2(\theta/2)(xX + yY + zZ) \\ &- 2\cos(\theta/2)\sin(\theta/2)[(n_x y - n_y x)Z + (n_y z - n_z y)X + (n_z x - n_x z)Y] \\ &+ \sin^2(\theta/2)(n_x xI + in_x yZ - in_x zY - in_y xZ + n_y yI + in_y zX + in_z xY - in_z yX + n_z zI) \\ &\times (n_x X + n_y Y + n_z Z)] \right\} \\ &= \frac{1}{2} \left\{ I + \cos^2(\theta/2)(xX + yY + zZ) - \sin(\theta)[(n_x y - n_y x)Z + (n_y z - n_z y)X + (n_z x - n_x z)Y] \\ &+ \sin^2(\theta/2)[(2n_x \hat{\mathbf{n}} \cdot \mathbf{r} - x)X + (2n_y \hat{\mathbf{n}} \cdot \mathbf{r} - y)Y + (2n_z \hat{\mathbf{n}} \cdot \mathbf{r} - z)Z] \right\} \end{split}$$

If we write $\mathbf{r} = (\hat{\mathbf{n}} \cdot \mathbf{r})\hat{\mathbf{n}} + \mathbf{r}_{\perp}$, where \mathbf{r}_{\perp} is the component of \mathbf{r} which is orthogonal to $\hat{\mathbf{n}}$, this becomes

$$\hat{\rho}' = \frac{1}{2} [I + (\hat{\mathbf{n}} \cdot \mathbf{r}) \hat{\mathbf{n}} \cdot \mathbf{X} + \cos \theta \mathbf{r}_{\perp} \cdot \mathbf{X} + \sin \theta (\mathbf{r}_{\perp} \times \hat{\mathbf{n}}) \cdot \mathbf{X}],$$

which indeed gives a rotation about $\hat{\mathbf{n}}$ by an angle θ .

Q.5 Compute the action of the circuits below on states in the computational basis. Give simpler equivalent circuits where possible.



- S.5 (a) First, recall the states $|\pm\rangle = \frac{1}{\sqrt{2}}(|0\rangle \pm |1\rangle)$. Then $|00\rangle \rightarrow |++\rangle \rightarrow |++\rangle \rightarrow |00\rangle$, $|01\rangle \rightarrow |+-\rangle \rightarrow |--\rangle \rightarrow |11\rangle$, $|10\rangle \rightarrow |-+\rangle \rightarrow |-+\rangle \rightarrow |10\rangle$, $|11\rangle \rightarrow |--\rangle \rightarrow |+-\rangle \rightarrow |01\rangle$. This is equivalent to CNOT with q_0 as the control bit.
 - (b) This is very straightforward to calculate directly for each computational basis state. $|00\rangle \rightarrow |10\rangle \rightarrow |11\rangle \rightarrow |01\rangle$. $|01\rangle \rightarrow |11\rangle \rightarrow |10\rangle \rightarrow |00\rangle$. $|10\rangle \rightarrow |00\rangle \rightarrow |00\rangle \rightarrow |10\rangle$. $|11\rangle \rightarrow |01\rangle \rightarrow |01\rangle \rightarrow |11\rangle$.

Alternatively, note that two NOT gates act on q_1 so it is unchanged. As it is used as the control after the first NOT, q_0 is changed precisely when initially $q_1 = 0$.

(c) This is easier to do if we use the result in part (a), together with the fact that $H^2 = I$ which allow us to add two Hadamard gates to q_0 to the left of the *CNOT* gate, to write it as



Then

$$\begin{split} |00\rangle \rightarrow |0+\rangle \rightarrow \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle) \rightarrow \frac{1}{\sqrt{2}}(|00\rangle - |11\rangle) \rightarrow \frac{1}{\sqrt{2}}(|0+\rangle - |1-\rangle) \\ |01\rangle \rightarrow |0-\rangle \rightarrow \frac{1}{\sqrt{2}}(|00\rangle - |11\rangle) \rightarrow \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle) \rightarrow \frac{1}{\sqrt{2}}(|0+\rangle + |1-\rangle) \\ |10\rangle \rightarrow |1+\rangle \rightarrow \frac{1}{\sqrt{2}}(|10\rangle + |01\rangle) \rightarrow \frac{1}{\sqrt{2}}(|10\rangle + |01\rangle) \rightarrow \frac{1}{\sqrt{2}}(|1+\rangle + |0-\rangle) \\ |11\rangle \rightarrow |1-\rangle \rightarrow \frac{1}{\sqrt{2}}(|10\rangle - |01\rangle) \rightarrow \frac{1}{\sqrt{2}}(|10\rangle - |01\rangle) \rightarrow \frac{1}{\sqrt{2}}(|1+\rangle - |0-\rangle) \end{split}$$

(d) If q_2 is zero, the circuit simplifies to just two CNOTs (control q_1 , target q_0), which is trivial. For $q_2 = 1$ you can directly calculate $|10q_0\rangle \rightarrow |10q_0\rangle \rightarrow |11q_0\rangle \rightarrow |11(q_0 \oplus 1)\rangle \rightarrow |10(q_0 \oplus 1)\rangle \rightarrow |10q_0\rangle$. $|11q_0\rangle \rightarrow |11(q_0 \oplus 1)\rangle \rightarrow |10(q_0 \oplus 1)\rangle \rightarrow |10(q_0 \oplus 1)\rangle \rightarrow |11(q_0 \oplus 1)\rangle \rightarrow |11q_0\rangle$. Alternatively, for $q_2 = 1$ note that q_2 and q_1 are not changes, since for q_1 we have two

Alternatively, for $q_2 = 1$ note that q_2 and q_1 are not changes, since for q_1 we have two NOTs which gives the identity. For q_0 since q_1 has a NOT between the two CNOTs where q_1 is the control, exactly one of them will act as NOT on q_0 . However, the final CNOT with control $q_2 = 1$ acts as another NOT on q_0 , so it is also unchanged.

Thus, the action in the computational basis is completely trivial. This is a trivial unitary. The circuit can then be simplified to simply 3 horizontal lines.

- Q.6 Show that $S = \frac{1}{2}(1 + X_iX_j + Y_iY_j + Z_iZ_j)$ defines a swap operator, interchanging the state of qubits *i* and *j*.
- S.6 Consider the action on computational basis states:
 - $X_i X_j |00\rangle = |11\rangle$, $Y_i Y_j |00\rangle = -|11\rangle$, $Z_i Z_j |00\rangle = |00\rangle$, so $S|00\rangle = |00\rangle$.
 - $X_i X_j |01\rangle = |10\rangle, Y_i Y_j |01\rangle = |10\rangle, Z_i Z_j |01\rangle = -|01\rangle$, so $S|01\rangle = |10\rangle$.
 - $X_i X_j |10\rangle = |01\rangle$, $Y_i Y_j |10\rangle = |01\rangle$, $Z_i Z_j |10\rangle = -|10\rangle$, so $S|10\rangle = |01\rangle$.
 - $X_i X_j |11\rangle = |00\rangle, Y_i Y_j |11\rangle = -|00\rangle, Z_i Z_j |11\rangle = |11\rangle$, so $S |11\rangle = |11\rangle$.

Alternatively, you could multiply out the matrices.

Q.7 By considering the action on computational basis states, show that the circuit given in lectures (and reproduced below) does implement the Toffoli gate (CCNOT).



S.7 For $q_2 = 0$ T does nothing to $|q_2\rangle$ while the phase gates on q_1 are $ST^{\dagger}T^{\dagger} = I$. For $q_1 = 0$, the action on q_0 is $HTT^{\dagger}TT^{\dagger}H = I$. For $q_1 = 1$, the action on q_0 is $HTT^{\dagger}XTT^{\dagger}XH = I$.

For $q_2 = 1$, the action on q_1 is $SXT^{\dagger}XT^{\dagger}$; for $q_1 = 0$ this is an $e^{-i\pi/4}$ phase which cancels the phase from the *T* acting on q_0 . For $q_1 = 1$ it is an $e^{i\pi/4}$ phase, so the upper two qubits contribute a $e^{i\pi/2}$ phase. For $q_2 = 1$, $q_1 = 0$ the action on q_0 is $HTXT^{\dagger}TXT^{\dagger}H = I$. For $q_2 = 1$, $q_1 = 1$, the action on q_0 is $HTXT^{\dagger}XT$. It seems easiest at this stage

to multiply out explicitly:
$$TX = \begin{pmatrix} 0 & 1 \\ e^{i\pi/4} & 0 \end{pmatrix}$$
 and $T^{\dagger}X = \begin{pmatrix} 0 & 1 \\ e^{-i\pi/4} & 0 \end{pmatrix}$ so $TXT^{\dagger}X = \begin{pmatrix} e^{-i\pi/4} & 0 \\ 0 & e^{i\pi/4} \end{pmatrix}$, hence $TXT^{\dagger}XTXT^{\dagger}X = \begin{pmatrix} e^{-i\pi/2} & 0 \\ 0 & e^{i\pi/2} \end{pmatrix} = -iZ$. So, the action on q_0 is $-iHZH = -iX$.

The overall phase cancels with the phase from the gates on q_2, q_1 . So this circuit acts as the identity on the states with $q_2 = 0$ or $q_1 = 0$, and when $q_2 = q_1 = 1$, it acts as NOT on q_0 , realising the Toffoli gate.

- Q.8 Consider a two-qubit system. Construct a circuit to realise the operation $U = \begin{pmatrix} T & 0 \\ 0 & X \end{pmatrix}$, where T, X are the standard 2 × 2 matrices.
- S.8 Acting on 2-qubit computational basis states $|q_1q_0\rangle$, this is T on $|q_0\rangle$ if the $q_1 = 0$, and X on $|q_0\rangle$ if the $q_1 = 1$. Hence we want



It is also correct to have the CNOT gate on the left.

- Q.9 Consider a two-qubit system. Construct a circuit to realise the operation $U = \begin{pmatrix} 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 1 & 0 & 0 & 0 \end{pmatrix}$
- S.9 This is just a NOT on both bits which you can see from the action of U on the computational basis states.

If you don't spot the simple solution above, the methodical approach is to write U as a product of unitary matrices which are each 2×2 unitary matrices U_{ij}^{\dagger} embedded in the 4×4 identity matrix, where $U_{ij} = U_{ji}$ has non-trivial entries in the *ii*, *ij*, *ji* and *jj* components only. We do this by multiplying U by suitable U_{ij} so that, working left to right and up to down, we set the off-diagonal components of U to 0, essentially by doing row reduction (but constrained since we can only use unitary matrices).

So, we start by choosing U_{14} to make the 4th element in the 1st row of $U_{14}U$ vanish. This requires the component $(U_{14})_{44} = 0$, so for unitarity we need $(U_{14})_{41} = (U_{14})_{14} = 1$ and then we see $(U_{14})_{11} = 0$. (Actually, we could have arbitrary phases for the 14 and 41 components, but we fix the 14 component to 1 so that the 11 component of $U_{14}U$ is 1, and it doesn't matter what the other phase is so we choose it to be simply 1.) So, we have

$$U_{14} = \begin{pmatrix} 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 \end{pmatrix} , \quad U_{14}U = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix}$$

To continue we could choose U_{23} so that the 32 component of $U_{23}U_{14}U$ vanishes. However, we see that $U_{14}U$ is already a unitary matrix with only a 2×2 non-trivial block so we define this to be U_{23}^{\dagger} and have $U_{14}U = U_{23}^{\dagger}$ leading to the result $U = U_{14}^{\dagger}U_{23}^{\dagger}$.

The unitary matrices U_{14} and U_{23} do not act on single qubits so we need to use Gray codes to convert the basis so that they do act on single qubits. Since U_{14} acts on the basis states $|00\rangle$ and $|11\rangle$ we can use the Gray code $00 \rightarrow 01 \rightarrow 11$. Similarly for U_{23} we can use $01 \rightarrow 00 \rightarrow 10$. These are both the same transformation where we use CNOT on $|q_0\rangle$ when the control bit $q_1 = 0$ which we may write as C_1NOT_0 . This is implemented in the circuit by $X_1C_1NOT_0X_1$.

In the new basis U_{14} is NOT on $|q_1\rangle$ when $q_0 = 1$ while U_{23} is also NOT on $|q_1\rangle$ but when $q_0 = 0$. Therefore the overall effect is just NOT on $|q_1\rangle$, i.e. X_1 . Finally we must transform back to the original basis, again using $X_1C_1NOT_0X_1$.

So, the final circuit is $(X_1C_1NOT_0X_1)X_1(X_1C_1NOT_0X_1) = X_1(C_1NOT_0X_1C_1NOT_0)X_1 = X_1(X_1X_0)X_1 = X_1X_0.$

Q.10 Consider a two-qubit system. We wish to construct a circuit to realise the operation

$$U = \begin{pmatrix} 1 & 0 & 0 & 0\\ 0 & \frac{1}{2} & \frac{1}{2} & \frac{1}{\sqrt{2}}\\ 0 & \frac{1}{2} & \frac{1}{2} & -\frac{1}{\sqrt{2}}\\ 0 & \frac{1}{\sqrt{2}} & -\frac{1}{\sqrt{2}} & 0 \end{pmatrix}$$

- (a) First decompose this operator in terms of unitary operators U_1 , U_2 , U_3 which each act non-trivially on a two-dimensional subspace of the Hilbert space, $U = U_1 U_2 U_3$.
- (b) Use CNOTs to convert the operators which do not act on a subspace corresponding to a single qubit into ones that do.
- (c) Draw the resulting quantum circuit.
- S.10 (a) As in the previous question, choose unitaries U_{ij} to transform U into the identity by row reduction. In this example only the lower right 3×3 block is non-trivial so really it is a 3×3 problem embedded into 4×4 matrices. The matrices we need are U_{23} , U_{24} and U_{34} which in the notation of the question can be chosen to be (note this is not unique so if you have 3 other matrices that are unitary, non-trivial only in 2×2 submatrices and multiply to give U, that is a valid alternative solution – you will end up with a different but equivalent quantum circuit, and it may or may not be obvious how to relate the different circuits)

$$U_1 = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 \\ 0 & \frac{1}{\sqrt{2}} & -\frac{1}{\sqrt{2}} & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix}, \quad U_2 = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & \frac{1}{\sqrt{2}} & 0 & \frac{1}{\sqrt{2}} \\ 0 & 0 & 1 & 0 \\ 0 & \frac{1}{\sqrt{2}} & 0 & -\frac{1}{\sqrt{2}} \end{pmatrix}, \quad U_3 = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{pmatrix}.$$

(b) U_2 is a controlled-Hadamard with target $|q_1\rangle$ and control $|q_0\rangle$. U_3 is a CNOT with target $|q_0\rangle$ and control $|q_1\rangle$. So it is only U_1 we need to address: it acts on the subspace spanned by $|01\rangle$ and $|10\rangle$. Acting with CNOT, this is $|01\rangle$ and $|11\rangle$, so it's CNOT U_2 CNOT.

$$|q_1\rangle \xrightarrow{\bullet} H \xrightarrow{\bullet} H$$
(c) $|q_0\rangle \xrightarrow{\bullet} \xrightarrow{\bullet} \xrightarrow{\bullet}$

Q.11 Defining the error $E(U,V) \equiv \max_{\psi} ||(U-V)|\psi\rangle||$, show that $E(R_{\hat{n}}(\alpha), R_{\hat{n}}(\beta)) = \frac{1}{\sqrt{2}}|1-e^{i(\alpha-\beta)}|$.

S.11 Without loss of generality, change our basis so that $\hat{n} = (0, 0, 1)$, so $R_{\hat{n}}(\alpha) = R_z(\alpha)$. In the Bloch sphere representation, this is represented as a rotation in the x - y plane, and the error is maximised if we consider vectors in the x - y plane, that is, we take **r** orthogonal to **n**. In terms of the state, this is

$$|\psi(\theta)\rangle = \frac{1}{\sqrt{2}}(|0\rangle + e^{i\theta}|1\rangle),$$

where θ is the angle in the x - y plane. $R_z(\alpha)$ acts as $\theta \to \theta + \alpha$.

$$||(R_{z}(\alpha) - R_{z}(\beta)|\psi(\theta)\rangle|| = |||\psi(\theta + \alpha)\rangle - |\psi(\theta + \beta)\rangle|| = \frac{1}{\sqrt{2}}|e^{i(\theta + \alpha)} - e^{i(\theta + \beta)}| = \frac{1}{\sqrt{2}}|1 - e^{i(\alpha - \beta)}|.$$

Q.12 Delayed measurement: In the discussion of quantum teleportation, observers were often required to perform operations which depended on the result of a measurement. In a quantum circuit, we would represent such actions by performing a measurement on one qubit and then acting with a unitary on another if the result of the measurement was 1.

Show that such an operation can always be replaced by a controlled-unitary gate, with the measurement postponed to the end of the computation.

S.12 If the first qubit is initially in a state $|q_1\rangle = \alpha |0\rangle + \beta |1\rangle$, and the second qubit is in a state $|q_2\rangle$, acting with a controlled-unitary gate will put the system in the state $\alpha |0\rangle \otimes |q_2\rangle + \beta |1\rangle \otimes U |q_2\rangle$. Measuring the first qubit, we either measure 0, leaving the second qubit in the state $|q_2\rangle$, or we measure 1, leaving the second qubit in the state $U|q_2\rangle$. Mathematically, this is equivalent to measuring the first qubit and then acting on the second qubit with U if the measurement result is 1. Also, in both cases the probabilities of these outcomes are $|\alpha|^2$ and $|\beta|^2$.

Actually, we should consider the more general case when the two qubits may be entangled. In that case we can always write the initial state as $\alpha |0\rangle \otimes |\phi\rangle + \beta |1\rangle \otimes |\psi\rangle$ but by exactly the same argument, either way we will measure 0 with probability $|\alpha|^2$ and get final state $|0\rangle \otimes |\phi\rangle$ or 1 with probability $|\beta|^2$ and get final state $|1\rangle \otimes |\psi\rangle$.

Of course, if the two qubits are spatially separated, it is very difficult to perform the joint quantum operation necessary to implement the controlled unitary. It is therefore often advantageous to actually perform measurements first and communicate the classical information instead. However, theoretically we can always do measurements at the end and this simplifies our discussion of quantum circuits since we can always first implement a unitary transformation and then at the end make measurements.